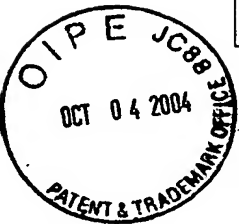


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(Marcus J. Millet)

Docket No.: TESSERA 3.0-336 II  
(PATENT)



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Haba et al.

Application No.: 10/786,819

Group Art Unit: 2811

Filed: February 25, 2004

Examiner: Not Yet Assigned

For: BALL GRID ARRAY WITH BUMPS

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

It is respectfully requested that the references listed on the enclosed form be made of record and considered with respect to the above-referenced U.S. patent application. In accordance with the notice at 1276 O.G. 55, copies of U.S. patent and U.S. patent publication references are not enclosed herewith; copies of other references are submitted herewith. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

In the event that any fee is due in connection with the present Information Disclosure Statement, the Commissioner is hereby authorized to charge the same to our Deposit Account No. 12-1095.

Dated:

Respectfully submitted,

By [Signature]

Marcus J. Millet

Registration No.: 28,241

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			Application Number	10/786,819	
			Filing Date	February 25, 2004	
			First Named Inventor	Belgacem Haba	
			Art Unit	2811	
			Examiner Name	Not Yet Assigned	
Sheet	1	of	4	Attorney Docket Number	TESSERA 3.0-336 II

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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Examiner Signature				Date Considered	



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				Application Number	10/786,819
				Filing Date	February 25, 2004
				First Named Inventor	Belgacem Haba
				Art Unit	2811
				Examiner Name	Not Yet Assigned
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Examiner Signature		Date Considered	
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Sheet	3	of	4	Attorney Docket Number	TESSERA 3.0-336 II

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Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				
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	BB	JP-05-251455	09-28-1993	SAKAE		✓
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NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T <sup>2</sup>
	CA	"Methods of Testing Chips and Joining Chips to Substrates," 2244 Research Disclosure, Feb. 1991, Elmsworth, GB, 32290.		
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	CD	LIN, CHARLES, SAM CHIANG, ANDREW YANG (Bridge Semiconductor Corp), "Bumpless 3D Stacked Packages," Advanced Technology Workshop on Advanced 3D Packaging, March 12, 2003.		
	CE	PFLUGHAUPT ET AL., PCT Application No. PCT/US02/32251, filed Oct. 9, 2002.		

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